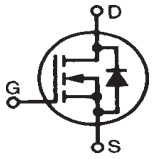
IXYS

Polar™ Power MOSFET

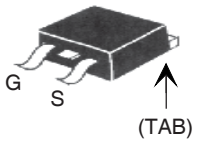
IXTA3N100P
IXTH3N100P
IXTP3N100P

V_{DSS} = 1000V
I_{D25} = 3.0A
$R_{DS(on)}$ ≤ 4.8Ω

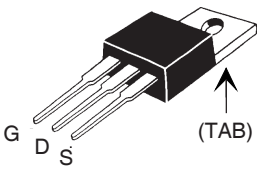
N-Channel Enhancement Mode
Avalanche Rated



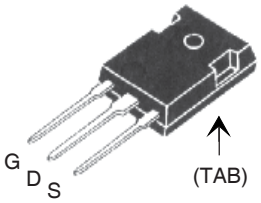
TO-263 (IXTA)



TO-220 (IXTP)



TO-247 (IXTH)



G = Gate D = Drain
S = Source TAB = Drain

Symbol	Test Conditions	Maximum Ratings	
V_{DSS}	T_J = 25°C to 150°C	1000	V
V_{DGR}	T_J = 25°C to 150°C, R_{GS} = 1MΩ	1000	V
V_{GSS}	Continuous	±20	V
V_{GSM}	Transient	±30	V
I_{D25}	T_C = 25°C	3	A
I_{DM}	T_C = 25°C, pulse width limited by T_{JM}	6	A
I_A	T_C = 25°C	3	A
E_{AS}	T_C = 25°C	200	mJ
dV/dt	$I_S \le I_{DM}$, $V_{DD} \le V_{DSS}$, $T_J \le 150°C$	10	V/ns
P_D	T_C = 25°C	125	W
T_J		-55 ... +150	°C
T_{JM}		150	°C
T_{stg}		-55 ... +150	°C
T_L	1.6mm (0.062) from case for 10s	300	°C
T_{SOLD}	Plastic body for 10s	260	°C
M_d	Mounting torque (TO-220)	1.13 / 10	Nm/lb.in.
Weight	TO-263	2.5	g
	TO-220	3.0	g
	TO-247	6.0	g

Symbol	Test Conditions (T_J = 25°C, unless otherwise specified)	Characteristic Values Min.	Typ.	Max.	
BV_{DSS}	V_{GS} = 0V, I_D = 250μA	1000			V
$V_{GS(th)}$	$V_{DS} = V_{GS}$, I_D = 250μA	2.5		4.5	V
I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V			±50	nA
I_{DSS}	$V_{DS} = V_{DSS}$, V_{GS} = 0V			5	μA
	T_J = 125°C			250	μA
$R_{DS(on)}$	V_{GS} = 10V, $I_D = 0.5 \bullet I_{D25}$, Note 1			4.8	Ω

Features

- International standard packages
- Unclamped Inductive Switching (UIS) rated
- Low package inductance
  - easy to drive and to protect

Advantages

- Easy to mount
- Space savings
- High power density

Applications:

- Switched-mode and resonant-mode power supplies
- DC-DC Converters
- Laser Drivers
- AC and DC motor controls
- Robotics and servo controls

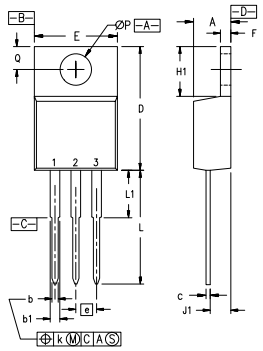
Symbol	Test Conditions (T_J = 25°C, unless otherwise specified)	Characteristic Values Min.	Typ.	Max
g_{fs}	V_{DS}= 20V, I_D = 0.5 • I_{D25}, Note 1	1.5	2.4	S
C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1MHz		1100	pF
C_{oss}			70	pF
C_{rss}			14.5	pF
$t_{d(on)}$	**Resistive Switching Times**		22	ns
t_r	V_{GS} = 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 0.5 • I_{D25}		27	ns
$t_{d(off)}$	R_G = 18Ω (External)		75	ns
t_f			29	ns
$Q_{g(on)}$	V_{GS}= 10V, V_{DS} = 0.5 • V_{DSS}, I_D = 0.5 • I_{D25}		39	nC
Q_{gs}			6	nC
Q_{gd}			20	nC
R_{thJC}				1.0 °C/W
R_{thCS}	(TO-220)		0.50	°C/W
	(TO-247)		0.21	°C/W

Source-Drain Diode

Symbol	Test Conditions (T_J = 25°C, unless otherwise specified)	Characteristic Values Min.	Typ.	Max.
I_S	V_{GS} = 0V			3 A
I_{SM}	Repetitive, pulse width limited by T_{JM}			9 A
V_{SD}	I_F = I_S, V_{GS} = 0V, Note 1			1.5 V
t_{rr}	I_F = 3A, -di/dt = 100A/μs, V_R = 100V, V_{GS} = 0V		820	ns

Note 1: Pulse test, t ≤ 300 μs; duty cycle, d ≤ 2%.

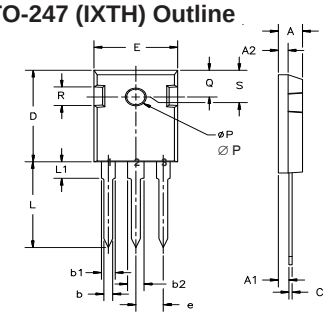
TO-220 (IXTP) Outline



Pins: 1 - Gate 2 - Drain

SYM	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX
A	.170	.190	4.32	4.83
b	.025	.040	0.64	1.02
b1	.045	.065	1.15	1.65
c	.014	.022	0.35	0.56
D	.580	.630	14.73	16.00
E	.390	.420	9.91	10.66
e	.100 BSC		2.54 BSC	
F	.045	.055	1.14	1.40
H1	.230	.270	5.85	6.85
J1	.090	.110	2.29	2.79
k	0	.015	0	0.38
L	.500	.550	12.70	13.97
L1	.110	.230	2.79	5.84
ØP	.139	.161	3.53	4.08
Q	.100	.125	2.54	3.18

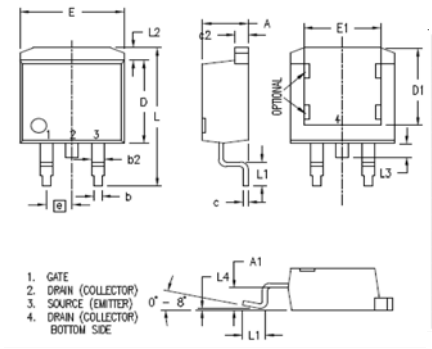
TO-247 (IXTH) Outline



Terminals: 1 - Gate 2 - Drain
3 - Source

Dim.	Millimeter Min.	Millimeter Max.	Inches Min.	Inches Max.
A	4.7	5.3	.185	.209
A_1	2.2	2.54	.087	.102
A_2	2.2	2.6	.059	.098
b	1.0	1.4	.040	.055
b_1	1.65	2.13	.065	.084
b_2	2.87	3.12	.113	.123
C	.4	.8	.016	.031
D	20.80	21.46	.819	.845
E	15.75	16.26	.610	.640
e	5.20	5.72	0.205	0.225
L	19.81	20.32	.780	.800
L1		4.50		.177
ØP	3.55	3.65	.140	.144
Q	5.89	6.40	0.232	0.252
R	4.32	5.49	.170	.216
S	6.15	BSC	242	BSC

TO-263 (IXTA) Outline



SYM	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX
A	.160	.190	4.06	4.83
A1	.080	.110	2.03	2.79
b	.020	.039	0.51	0.99
b2	.045	.055	1.14	1.40
c	.016	.029	0.40	0.74
c2	.045	.055	1.14	1.40
D	.340	.380	8.64	9.65
D1	.315	.350	8.00	8.89
E	.380	.410	9.65	10.41
E1	.245	.320	6.22	8.13
e	.100 BSC		2.54 BSC	
L	.575	.625	14.61	15.88
L1	.090	.110	2.29	2.79
L2	.040	.055	1.02	1.40
L3	.050	.070	1.27	1.78
L4	0	.005	0	0.13

IXYS reserves the right to change limits, test conditions, and dimensions.

Fig. 1. Output Characteristics @ 25ºC

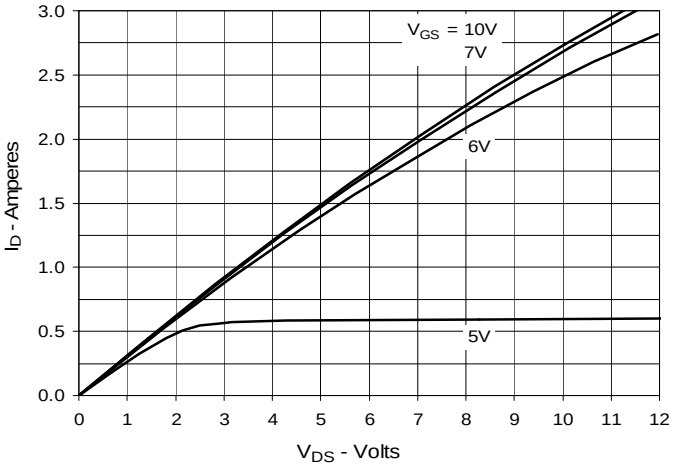


Fig. 2. Extended Output Characteristics @ 25ºC

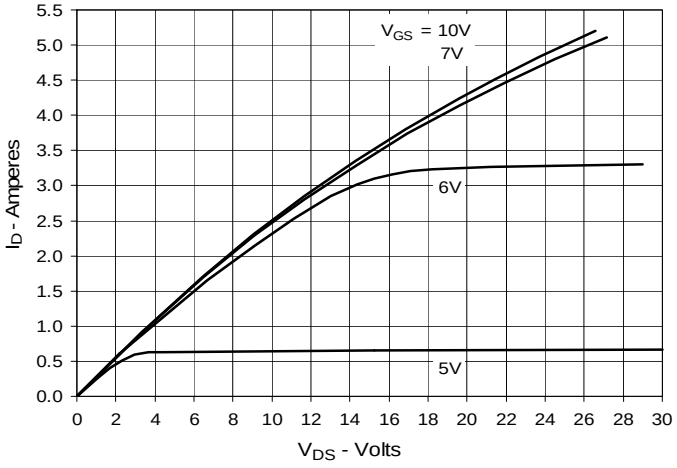


Fig. 3. Output Characteristics @ 125ºC

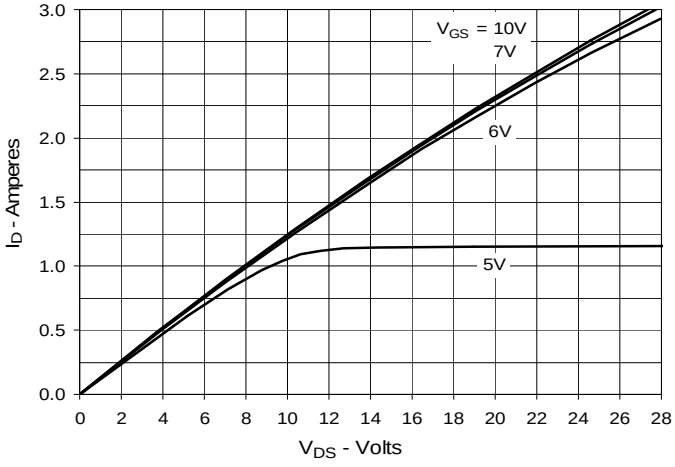


Fig. 4. $R_{DS(on)}$ Normalized to I_D = 1.5A Value vs. Junction Temperature

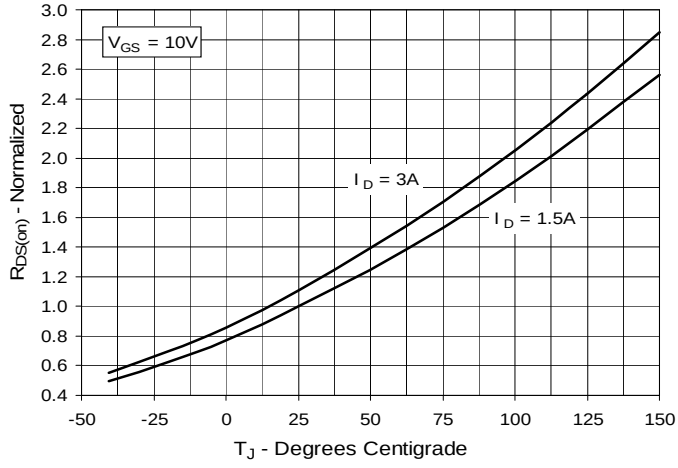


Fig. 5. $R_{DS(on)}$ Normalized to I_D = 1.5A Value vs. Drain Current

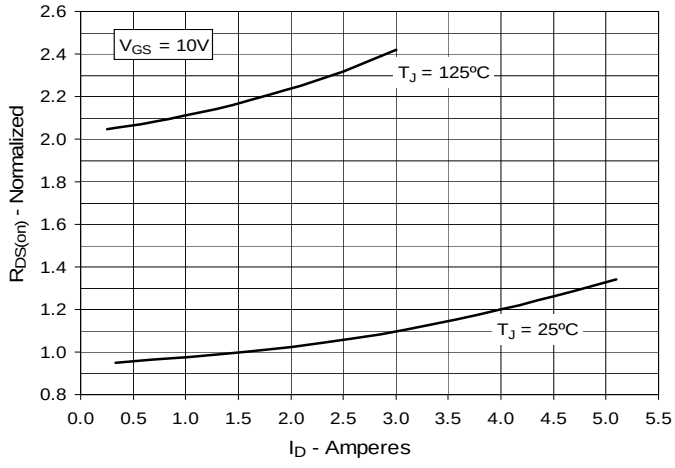


Fig. 6. Maximum Drain Current vs. Case Temperature

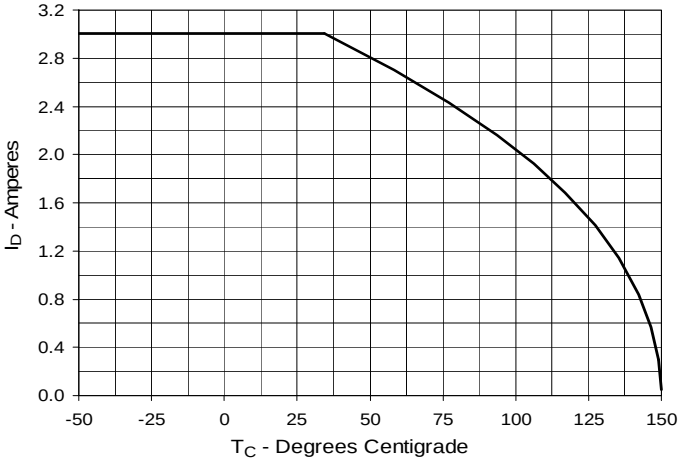


Fig. 7. Input Admittance

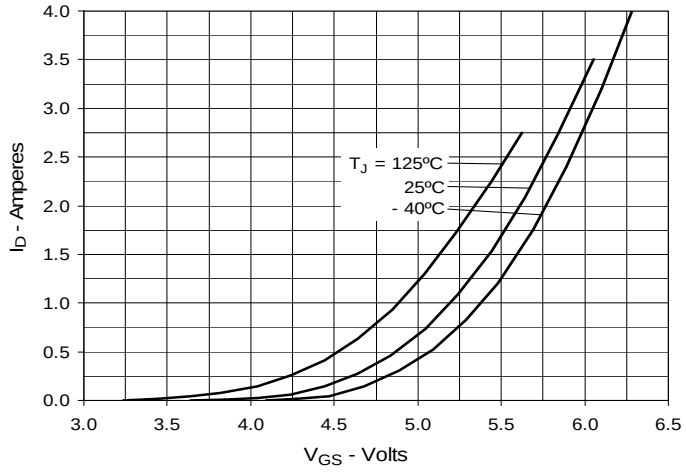


Fig. 8. Transconductance

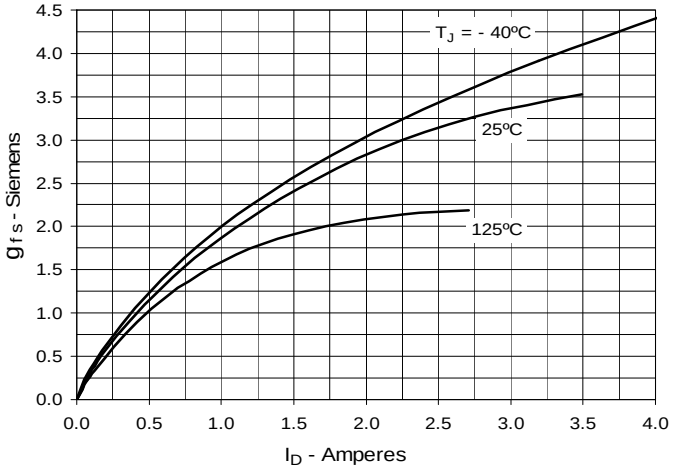


Fig. 9. Forward Voltage Drop of Intrinsic Diode

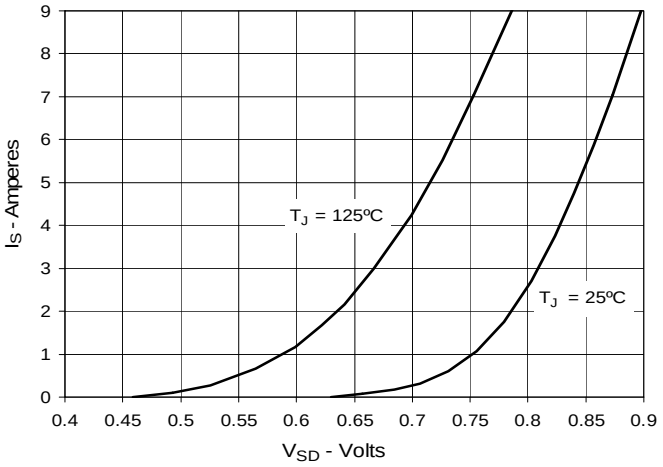


Fig. 10. Gate Charge

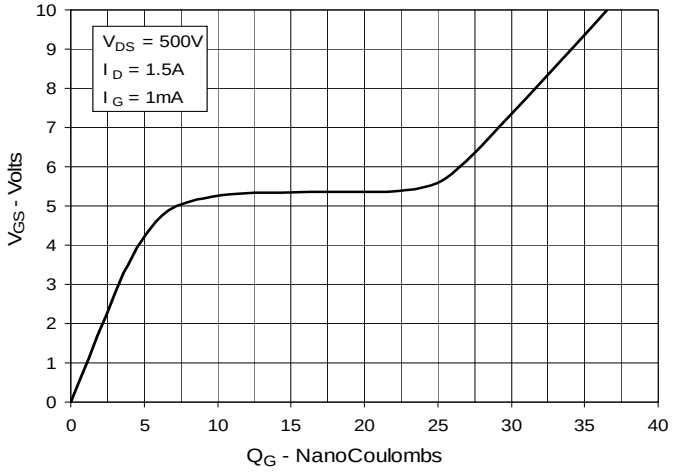


Fig. 11. Capacitance

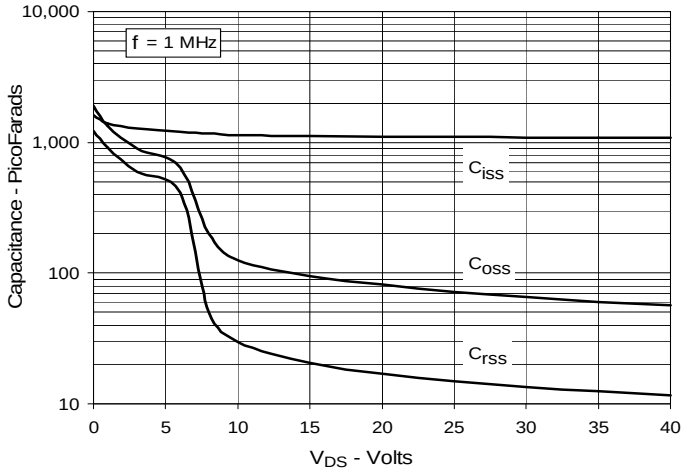


Fig. 12. Maximum Transient Thermal Impedance