

# New 8FX MCU Series Products 8pins~80pins



## Special features of 8FX MCU

### High quality & High reliability & Safety

- Number of Flash total write/erase cycles: 100,000
- Operating temperature range: -40~+85°C
- Flash contents protection
- Low voltage reset

### Wide package support.

- 8pins to 80 pins products.
- SOP, DIP, TSSOP, LQFP and QFN package.

### Easy-to-use

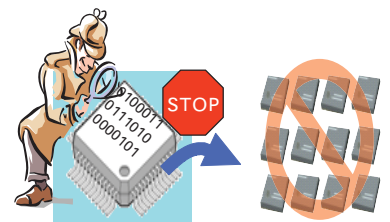
- Low-cost development environment
- Internal CR oscillator clock
- (an accuracy of  $\pm 2\%$  is guaranteed after chip mounted on board \*1)
- On-Chip Debugger

### Web site support

- Hardware manual/datasheets
- Application notes
- Sample software
- SOFTUNE free downloads
- FAQ
- Electrical characteristics (ESD/latch up data)
- Starter kit/tool manual

## FLASH Technology

- Reliability / quality of our FLASH memory
- Program cycle: 100,000 times
- Data retention time: 20 years
- High Reliability: Same technology for Automotive / Consumer applications.
- Flash Contents Protection
- Protect customer's property



\*1: in the case of Fujitsu's recommend mounted condition

Fujitsu Semiconductor MCU Selection Table 8-20 pins

Fujitsu MB95260 Series select guide

Part number	Pin count	Application	Operation voltage V <sub>CC</sub> (V)	ROM/RAM	LVD	I/O	Timer	LIN-UART	8/10bit A/D	Package
MB95F264HPF-G-SNE2	20	Standard	2.4~5.5	20KB/496B	-	16	2ch, The timer can be configured as an "8-bit timer×2 channels" or a "16-bit timer×1 channel". It has built-in timer function, PWC PWM function and input capture function. Count clock: it can be selected from internal clocks (seven types) and external clocks. It can output square wave.	1ch, A wide range of communication speed can be selected by a dedicated reload timer. It has a full duplex double buffer. Clock-synchronized serial data transfer and clock-asynchronized serial data transfer is enabled. The LIN function can be used as a LIN master or a LIN slave.	6ch, 8-bit or 10-bit resolution can be selected.	SOP20
MB95F264HPFT-G-SNE2				TSSOP20						
MB95F263HPF-G-SNE2				SOP20						
MB95F263HPFT-G-SNE2				TSSOP20						
MB95F262HPF-G-SNE2				SOP20						
MB95F262HPFT-G-SNE2				TSSOP20						
MB95F264KPF-G-SNE2				○	17	20KB/496B				SOP20
MB95F264KPFT-G-SNE2						TSSOP20				
MB95F263KPF-G-SNE2						SOP20				
MB95F263KPFT-G-SNE2						TSSOP20				
MB95F262KPF-G-SNE2						SOP20				
MB95F262KPFT-G-SNE2						TSSOP20				
MB95F274HPF-G-SNE2	8	Standard	2.4~5.5	20KB/496B	-	4	1ch, The timer can be configured as an "8-bit timer×2 channels" or a "16-bit timer×1 channel". It has built-in timer function, PWC PWM function and input capture function. Count clock: it can be selected from internal clocks (seven types) and external clocks. It can output square wave.	No LIN-UART.	2ch, 8-bit or 10-bit resolution can be selected.	SOP8
MB95F273HPF-G-SNE2				SOP8						
MB95F272HPF-G-SNE2				SOP8						
MB95F274KPF-G-SNE2				○	5	20KB/496B				SOP8
MB95F273KPF-G-SNE2						SOP8				
MB95F272KPF-G-SNE2						SOP8				
MB95F284HPF-G-SNE1	16	Standard	2.4~5.5	20KB/496B	-	12	1ch, The timer can be configured as an "8-bit timer×2 channels" or a "16-bit timer×1 channel". It has built-in timer function, PWC PWM function and input capture function. Count clock: it can be selected from internal clocks (seven types) and external clocks. It can output square wave.	1ch, A wide range of communication speed can be selected by a dedicated reload timer. It has a full duplex double buffer. Clock-synchronized serial data transfer and clock-asynchronized serial data transfer is enabled. The LIN function can be used as a LIN master or a LIN slave.	5ch, 8-bit or 10-bit resolution can be selected.	SOP16
MB95F283HPF-G-SNE1				SOP16						
MB95F282HPF-G-SNE1				SOP16						
MB95F284KPF-G-SNE1				○	13	20KB/496B				SOP16
MB95F283KPF-G-SNE1						SOP16				
MB95F282KPF-G-SNE1						SOP16				

Fujitsu MB95560 Series select guide

Part Number	Pin count	Application	Operation Voltage (V)	ROM/RAM	LVD	I/O	Timer	LIN-UART	8/10bit AD	Package
MB95F564HPF-G-SNE2	20	Standard	2.4~5.5	20KB/496B	-	16	2ch, The timer can be configured as an "8-bit timer×2 channels" or a "16-bit timer×1 channel". It has built-in timer function, PWC PWM function and input capture function. Count clock: it can be selected from internal clocks (seven types) and external clocks. It can output square wave.	1ch, A wide range of communication speed can be selected by a dedicated reload timer. It has a full duplex double buffer. Clock-synchronized serial data transfer and clock-asynchronized serial data transfer is enabled. The LIN function can be used as a LIN master or a LIN slave.	6ch, 8-bit or 10-bit resolution can be selected AD working range 2.7-5.5V	SOP20
MB95F564HPFT-G-SNE2				TSSOP20						
MB95F563HPF-G-SNE2				SOP20						
MB95F563HPFT-G-SNE2				TSSOP20						
MB95F562HPF-G-SNE2				SOP20						
MB95F562HPFT-G-SNE2				TSSOP20						
MB95F564KPF-G-SNE2				○	17	20KB/496B				SOP20
MB95F564KPFT-G-SNE2						TSSOP20				
MB95F563KPF-G-SNE2						SOP20				
MB95F563KPFT-G-SNE2						TSSOP20				
MB95F562KPF-G-SNE2						SOP20				
MB95F562KPFT-G-SNE2						TSSOP20				
MB95F574HPF-G-SNE2	8	Internal CR accuracy 2%	2.4~5.5	20KB/496B	○	4	1ch, The timer can be configured as an "8-bit timer×2 channels" or a "16-bit timer×1 channel". It has built-in timer function, PWC PWM function and input capture function. Count clock: it can be selected from internal clocks (seven types) and external clocks. It can output square wave.	No LIN-UART	2ch, 8-bit or 10-bit resolution can be selected AD working range 2.7-5.5V	SOP8
MB95F573HPF-G-SNE2				SOP8						
MB95F572HPF-G-SNE2				SOP8						
MB95F574KPF-G-SNE2				○	5	20KB/496B				SOP8
MB95F573KPF-G-SNE2						SOP8				
MB95F572KPF-G-SNE2						SOP8				
MB95F584HPF-G-SNE1	16	Standard	2.4~5.5	20KB/496B	○	12	1ch, The timer can be configured as an "8-bit timer×2 channels" or a "16-bit timer×1 channel". It has built-in timer function, PWC PWM function and input capture function. Count clock: it can be selected from internal clocks (seven types) and external clocks. It can output square wave.	1ch, A wide range of communication speed can be selected by a dedicated reload timer. It has a full duplex double buffer. Clock-synchronized serial data transfer and clock-asynchronized serial data transfer is enabled. The LIN function can be used as a LIN master or a LIN slave.	5ch, 8-bit or 10-bit resolution can be selected AD working range 2.7-5.5V	SOP16
MB95F583HPF-G-SNE1				SOP16						
MB95F582HPF-G-SNE1				SOP16						
MB95F584KPF-G-SNE1				○	13	20KB/496B				SOP16
MB95F583KPF-G-SNE1						SOP16				
MB95F582KPF-G-SNE1						SOP16				

### Fujitsu MB95350 Series select guide

Part number	Pin count	Application	Operation voltage Vcc(V)	ROM/RAM	LVD	I/O	Timer	LIN-UART	8/10bit A/D	Package
MB95F354LPF-G-SNE2	24	Standard	1.8~3.6	20KB/496B	-	21	<ul style="list-style-type: none"> <li>•8/16-bit composite timer×2 channels</li> <li>•Time-base timer×1 channel</li> <li>•Watch prescaler×1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO×1 channel</li> <li>•I<sup>2</sup>C×1 channel</li> <li>•LIN-UART×1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter×6 channels</li> </ul>	SOP24
MB95F354LPFT-G-SNE2										TSSOP24
MB95F354LWQN-G-SN(EF/ER)E1										QFN32
MB95F353LPF-G-SNE2										SOP24
MB95F353LPFT-G-SNE2										TSSOP24
MB95F353LWQN-G-SN(EF/ER)E1										QFN32
MB95F352LPF-G-SNE2										SOP24
MB95F352LPFT-G-SNE2										TSSOP24
MB95F352LWQN-G-SN(EF/ER)E1										QFN32
MB95F354EPF-G-SNE2										24
MB95F354EPFT-G-SNE2	TSSOP24									
MB95F354EWQN-G-SN(EF/ER)E1	QFN32									
MB95F353EPF-G-SNE2	SOP24									
MB95F353EPFT-G-SNE2	TSSOP24									
MB95F353EWQN-G-SN(EF/ER)E1	QFN32									
MB95F352EPF-G-SNE2	SOP24									
MB95F352EPFT-G-SNE2	TSSOP24									
MB95F352EWQN-G-SN(EF/ER)E1	QFN32									
MB95F352EWQN-G-SN(EF/ER)E1	QFN32									

### Fujitsu MB95430 Series select guide

Part number	Pin count	Application	Operation voltage Vcc(V)	ROM/RAM	LVD	I/O	Timer	LIN-UART	8/10bit A/D	Speical	Package
MB95F434HP-G-SH-SNE2	32	Standard	2.4~5.5	20KB/496B	-	28	<ul style="list-style-type: none"> <li>•8/16-bit composite timer×1 channel</li> <li>•16-bit PPG×1 channel</li> <li>•16-bit free-running timer×1 channel</li> <li>•16-bit output compare×2 channels</li> <li>•Time-base timer×1 channel</li> <li>•Watch prescaler×1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO×1 channel</li> <li>•I<sup>2</sup>C×1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter×8 channels</li> </ul>	<ul style="list-style-type: none"> <li>•Voltage comparator×4 channel</li> <li>•Operational amplifier (OPAMP)×1 channel</li> </ul>	SDIP32
MB95F434HPMC-G-SNE2											LQFP32
MB95F433HP-G-SH-SNE2											SDIP32
MB95F433HPMC-G-SNE2											LQFP32
MB95F432HP-G-SH-SNE2											SDIP32
MB95F432HPMC-G-SNE2											LQFP32
MB95F434KP-G-SH-SNE2											SDIP32
MB95F434KPMC-G-SNE2											LQFP32
MB95F433KP-G-SH-SNE2											SDIP32
MB95F433KPMC-G-SNE2											LQFP32
MB95F432KP-G-SH-SNE2	SDIP32										
MB95F432KPMC-G-SNE2	LQFP32										

### Fujitsu MB95330 Series select guide

Part number	Pin count	Application	Operation voltage Vcc(V)	ROM/RAM	LVD	I/O	Timer	Communication	8/10bit A/D	Speical	Package
MB95F334HPMC-G-SNE2	32	Standard	2.4~5.5	20KB/1008	-	28	<ul style="list-style-type: none"> <li>•8/16-bit composite timer×2 channels</li> <li>•8/16-bit PPG×3 channels</li> <li>•16-bit PPG×1 channel (can work independently or together with the multi-pulse generator)</li> <li>•16-bit reload timer×1 channel (can work independently or together with the multi-pulse generator)</li> <li>•Timebase timer×1 channel</li> <li>•Watch prescaler×1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO×1 channel</li> <li>•LIN-UART×1 channel</li> <li>•I<sup>2</sup>C×1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter×8 channels</li> </ul>	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control) × 1 channel</li> <li>•Dual operation Flash memory</li> </ul>	LQFP32
MB95F334HP-G-SH-SNE2											SDIP32
MB95F334HWQN-G-SNERE1											QFN32
MB95F334KPMC-G-SNE2											LQFP32
MB95F334KP-G-SH-SNE2											SDIP32
MB95F334KWQN-G-SNERE1											QFN32
MB95F333HPMC-G-SNE2											LQFP32
MB95F333HP-G-SH-SNE2											SDIP32
MB95F333HWQN-G-SNERE1											QFN32
MB95F333KPMC-G-SNE2											LQFP32
MB95F333KP-G-SH-SNE2	SDIP32										
MB95F333KWQN-G-SNERE1	QFN32										
MB95F332HPMC-G-SNE2	32	Standard	2.4~5.5	8KB/240B	-	28	<ul style="list-style-type: none"> <li>•Timebase timer×1 channel</li> <li>•Watch prescaler×1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO×1 channel</li> <li>•LIN-UART×1 channel</li> <li>•I<sup>2</sup>C×1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter×8 channels</li> </ul>	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control) × 1 channel</li> <li>•Dual operation Flash memory</li> </ul>	LQFP32
MB95F332HP-G-SH-SNE2											SDIP32
MB95F332HWQN-G-SNERE1											QFN32
MB95F332KPMC-G-SNE2											LQFP32
MB95F332KP-G-SH-SNE2											SDIP32
MB95F332KWQN-G-SNERE1											QFN32

### Fujitsu MB95390 Series select guide

Part number	Pin count	Application	Operation voltage Vcc(V)	ROM/RAM	LVD	I/O	Timer	Communication	8/10bit A/D	Speical	Package	Packing
MB95F398HPMC-G-SNE2	48	Standard	2.4~5.5	60KB/2032B	-	44	<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG×3 channels</li> <li>•16-bit PPG×1 channel (can work independently or together with the multi-pulse generator)</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO × 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C×1 channel</li> </ul>	•8/10-bit A/D converter × 12 channels	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control)×1 channel</li> <li>•Dual operation Flash memory</li> </ul>	LQFP48	Tray
MB95F398HWQN-G-SNE1											QFN48	Tray
MB95F398HWQN-G-SNERE1											QFN48	Tape
MB95F398KPMC-G-SNE2											LQFP48	Tray
MB95F398KWQN-G-SNE1											QFN48	Tray
MB95F398KWQN-G-SNERE1											QFN48	Tape
MB95F396HPMC-G-SNE2	48	Standard	2.4~5.5	36KB/1008B	-	44	<ul style="list-style-type: none"> <li>•16-bit reload timer×1 channel (can work independently or together with the multi-pulse generator)</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO × 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C×1 channel</li> </ul>	•8/10-bit A/D converter × 12 channels	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control)×1 channel</li> <li>•Dual operation Flash memory</li> </ul>	LQFP48	Tray
MB95F396HWQN-G-SNE1											QFN48	Tray
MB95F396HWQN-G-SNERE1											QFN48	Tape
MB95F396KPMC-G-SNE2											LQFP48	Tray
MB95F396KWQN-G-SNE1											QFN48	Tray
MB95F396KWQN-G-SNERE1											QFN48	Tape
MB95F394HPMC-G-SNE2	48	Standard	2.4~5.5	20KB/496B	-	44	<ul style="list-style-type: none"> <li>•Timebase timer× 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO × 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C×1 channel</li> </ul>	•8/10-bit A/D converter × 12 channels	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control)×1 channel</li> <li>•Dual operation Flash memory</li> </ul>	LQFP48	Tray
MB95F394HWQN-G-SNE1											QFN48	Tray
MB95F394HWQN-G-SNERE1											QFN48	Tape
MB95F394KPMC-G-SNE2											LQFP48	Tray
MB95F394KWQN-G-SNE1											QFN48	Tray
MB95F394KWQN-G-SNERE1											QFN48	Tape

### Fujitsu MB95310/370 Series select guide

Part number	Pin count	Application	Operation voltage Vcc(V)	ROM/RAM	LVD	I/O	Timer	Communication	8/10bit A/D	Speical	Package	
MB95F314EPMC-G-SNE2	80	Standard	1.8~3.6	20KB/496B	○	71	<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG × 2 channels (Each channel of PPG can be configured as "8-bitPPG × 2 Channels" or 16-bit PPG×1 channel")</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO × 2 channels</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	•8/10-bit A/D converter × 4 channels	COM:4 (Max) SEG:40(Max) 40SEG × 4 COM:160 pixels can be displayed	LQFP80	
MB95F314LPMC-G-SNE2												-
MB95F316EPMC-G-SNE2												○
MB95F316LPMC-G-SNE2												-
MB95F318EPMC-G-SNE2												○
MB95F318LPMC-G-SNE2												-
MB95F374EPMC2-G-SNE2	64	Segment LCD display	1.8~3.6	20KB/496B	○	55	<ul style="list-style-type: none"> <li>•16-bit reload timer × 1 channel</li> <li>•Timebase timer × 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO × 2 channels</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	•8/10-bit A/D converter × 4 channels	COM:4 (Max) SEG:32(Max) 32SEG × 4 COM:128 pixels can be displayed	LQFP64(0.65mm)	
MB95F374EPMC1-G-SNE2											-	
MB95F374LPMC2-G-SNE2											-	
MB95F374LPMC1-G-SNE2											-	
MB95F376EPMC2-G-SNE2											○	
MB95F376EPMC1-G-SNE2											-	
MB95F376LPMC2-G-SNE2											-	
MB95F376LPMC1-G-SNE2				-								
MB95F378EPMC2-G-SNE2				○								
MB95F378EPMC1-G-SNE2				-								
MB95F378LPMC2-G-SNE2				-								
MB95F378LPMC1-G-SNE2				-								
MB95F378LPMC1-G-SNE2				○								
MB95F378LPMC1-G-SNE2				-								

### Fujitsu MB95410/470 Series select guide

Part number	Pin count	Application	Operation voltage (v)	ROM/RAM	LVD	I/O	Timer	Communication	8/10bit A/D	Other function	Package	
MB95F414KPMC-G-SNE2	80	Standard	2.4~5.5	20KB/496B	○	75	<ul style="list-style-type: none"> <li>•8/16-bit composite timer 2 ch</li> <li>•8/16-bit PPG 2ch (PPG can be used as "8-bit PPG × 2 channels" or "16-bit PPG × 1 channel)</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO × 3 channels</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	8/10-bit A/D converter × 8channels	LCDC: COM output: 4 or 8 (Max) (selectable) SEG output: 40 or 36 (Max) (selectable) 36 SEG × 8 COM: 288 pixels can be displayed Voltage comparator: 1ch	LQFP80(0.5mm)	
MB95F414HPMC-G-SNE2												74
MB95F416KPMC-G-SNE2												○
MB95F416HPMC-G-SNE2												75
MB95F418KPMC-G-SNE2												○
MB95F418HPMC-G-SNE2												74
MB95F474KPMC1-G-SNE2	64	Standard	2.4~5.5	20KB/496B	○	58	<ul style="list-style-type: none"> <li>•16-bit reload timer 1ch</li> <li>•Time-base timer 1ch</li> <li>•Watch prescaler 1ch</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO × 3 channels</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	8/10-bit A/D converter × 8channels	LCDC: COM output: 4 or 8 (Max) (selectable) SEG output: 32 or 28 (Max) (selectable) 28 SEG × 8 COM: 224 pixels can be displayed Voltage comparator: 1ch	LQFP64(0.5mm)	
MB95F474KPMC2-G-SNE2											-	
MB95F474HPMC1-G-SNE2											-	
MB95F474HPMC2-G-SNE2											-	
MB95F476KPMC1-G-SNE2											○	
MB95F476KPMC1-G-SNE2											-	
MB95F476HPMC1-G-SNE2											-	
MB95F476HPMC2-G-SNE2				-								
MB95F478KPMC1-G-SNE2				○								
MB95F478KPMC2-G-SNE2				-								
MB95F478HPMC1-G-SNE2				-								
MB95F478HPMC2-G-SNE2				-								
MB95F478HPMC2-G-SNE2				○								
MB95F478HPMC2-G-SNE2				58								

### Fujitsu MB95630 Series select guide

Part number	Pin count	ROM/RAM	LVD (optional)	I/O	Timer	Communication	8/10bit A/D	Speical	Package												
MB95F636HPMC-G-SNE2	32	36KB/1024B	-	28	<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG × 3 channels</li> <li>•16-bit PPG × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•16-bit reload timer × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•Timebase timer × 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO× 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter × 8 channels</li> </ul>	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control) × 1 channel</li> <li>•Analog comparator</li> </ul>	LQFP32												
MB95F636HP-G-SH-SNE2			SDIP32																		
MB95F636HWQN-G-SNE1			QFN32																		
MB95F636KPMC-G-SNE2			LQFP32																		
MB95F636KP-G-SH-SNE2			SDIP32																		
MB95F636KWQN-G-SNE1			QFN32																		
MB95F634HPMC-G-SNE2		20KB/1024B	-	28					<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG × 3 channels</li> <li>•16-bit PPG × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•16-bit reload timer × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•Timebase timer × 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO× 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter × 8 channels</li> </ul>	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control) × 1 channel</li> <li>•Analog comparator</li> </ul>	LQFP32								
MB95F634HP-G-SH-SNE2			SDIP32																		
MB95F634HWQN-G-SNE1			QFN32																		
MB95F634KPMC-G-SNE2			LQFP32																		
MB95F634KP-G-SH-SNE2			SDIP32																		
MB95F634KWQN-G-SNE1			QFN32																		
MB95F633HPMC-G-SNE2		12KB/512B	-	28									<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG × 3 channels</li> <li>•16-bit PPG × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•16-bit reload timer × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•Timebase timer × 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO× 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter × 8 channels</li> </ul>	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control) × 1 channel</li> <li>•Analog comparator</li> </ul>	LQFP32				
MB95F633HP-G-SH-SNE2			SDIP32																		
MB95F633HWQN-G-SNE1			QFN32																		
MB95F633KPMC-G-SNE2			LQFP32																		
MB95F633KP-G-SH-SNE2			SDIP32																		
MB95F633KWQN-G-SNE1			QFN32																		
MB95F632HPMC-G-SNE2		8KB/256B	-	28													<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG × 3 channels</li> <li>•16-bit PPG × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•16-bit reload timer × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•Timebase timer × 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO× 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter × 8 channels</li> </ul>	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control) × 1 channel</li> <li>•Analog comparator</li> </ul>	LQFP32
MB95F632HP-G-SH-SNE2			SDIP32																		
MB95F632HWQN-G-SNE1	QFN32																				
MB95F632KPMC-G-SNE2	LQFP32																				
MB95F632KP-G-SH-SNE2	SDIP32																				
MB95F632KWQN-G-SNE1	QFN32																				

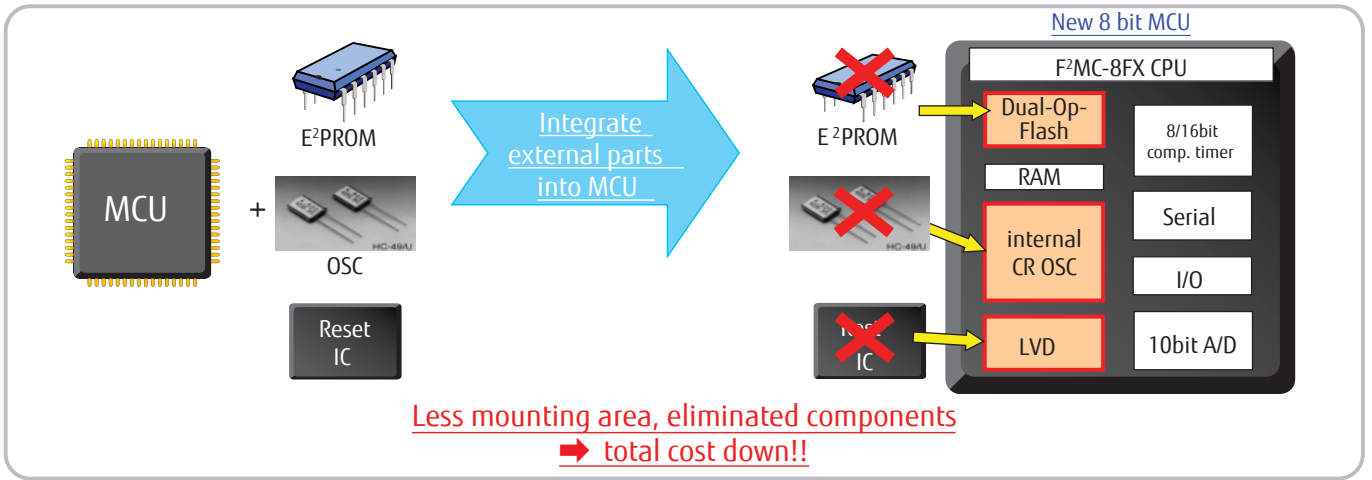
### Fujitsu MB95690 Series select guide

Part number	Pin count	ROM/RAM	LVD (optional)	I/O	Timer	Communication	8/10bit A/D	Speical	Package												
MB95F698KPMC-G-SNE2	48	60KB/2KB	○	45(Max)	<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG × 3 channels</li> <li>•16-bit PPG × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•16-bit reload timer×1 channel (can work independently or together with the multi-pulse generator)</li> <li>•Timebase timer × 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO× 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter × 12channels</li> </ul>	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control) × 1 channel</li> <li>•Analog comparator</li> </ul>	LQFP48												
MB95F698KPMC1-G-SNE2	52								LQFP52												
MB95F698KWQN-G-SNE1	48								QFN48												
MB95F698KWQN-G-SNERE1									QFN48(Tape&Reel)												
MB95F696KPMC-G-SNE2	48	36KB/1KB							○	45(Max)	<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG × 3 channels</li> <li>•16-bit PPG × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•16-bit reload timer×1 channel (can work independently or together with the multi-pulse generator)</li> <li>•Timebase timer × 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO× 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter × 12channels</li> </ul>	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control) × 1 channel</li> <li>•Analog comparator</li> </ul>	LQFP48						
MB95F696KPMC1-G-SNE2	52														LQFP52						
MB95F696KWQN-G-SNE1	48														QFN48						
MB95F696KWQN-G-SNERE1															QFN48(Tape&Reel)						
MB95F694KPMC-G-SNE2	48	20KB/512B													○	45(Max)	<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG × 3 channels</li> <li>•16-bit PPG × 1 channel (can work independently or together with the multi-pulse generator)</li> <li>•16-bit reload timer×1 channel (can work independently or together with the multi-pulse generator)</li> <li>•Timebase timer × 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO× 1 channel</li> <li>•LIN-UART× 1 channel</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter × 12channels</li> </ul>	<ul style="list-style-type: none"> <li>•Multi-pulse generator (for DC motor control) × 1 channel</li> <li>•Analog comparator</li> </ul>	LQFP48
MB95F694KPMC1-G-SNE2	52																				LQFP52
MB95F694KWQN-G-SNE1	48																				QFN48
MB95F694KWQN-G-SNERE1																					QFN48(Tape&Reel)

### Fujitsu MB95810 Series select guide

Part number	Pin count	ROM/RAM	LVD (optional)	I/O	Timer	Communication	8/10bit A/D	Speical	Package
MB95F818KPMC1-G-SNE2	64	60KB/2KB	○	58(Max)	<ul style="list-style-type: none"> <li>•8/16-bit composite timer × 2 channels</li> <li>•8/16-bit PPG × 2 channels</li> <li>•16-bit PPG × 2 channels</li> <li>•16-bit reload timer × 1 channel</li> <li>•Timebase timer × 1 channel</li> <li>•Watch prescaler × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•UART/SIO× 1 channel</li> <li>•I<sup>2</sup>C × 1 channel</li> </ul>	<ul style="list-style-type: none"> <li>•8/10-bit A/D converter × 12 channels</li> </ul>	<ul style="list-style-type: none"> <li>•Analog comparator × 2 channels</li> </ul>	LQFP64(0.5mm)
MB95F818KPMC2-G-SNE2									LQFP64(0.65mm)
MB95F816KPMC1-G-SNE2		32KB/1KB							LQFP64(0.5mm)
MB95F816KPMC2-G-SNE2									LQFP64(0.65mm)
MB95F814KPMC1-G-SNE2		16KB/512KB							LQFP64(0.5mm)
MB95F814KPMC2-G-SNE2									LQFP64(0.65mm)

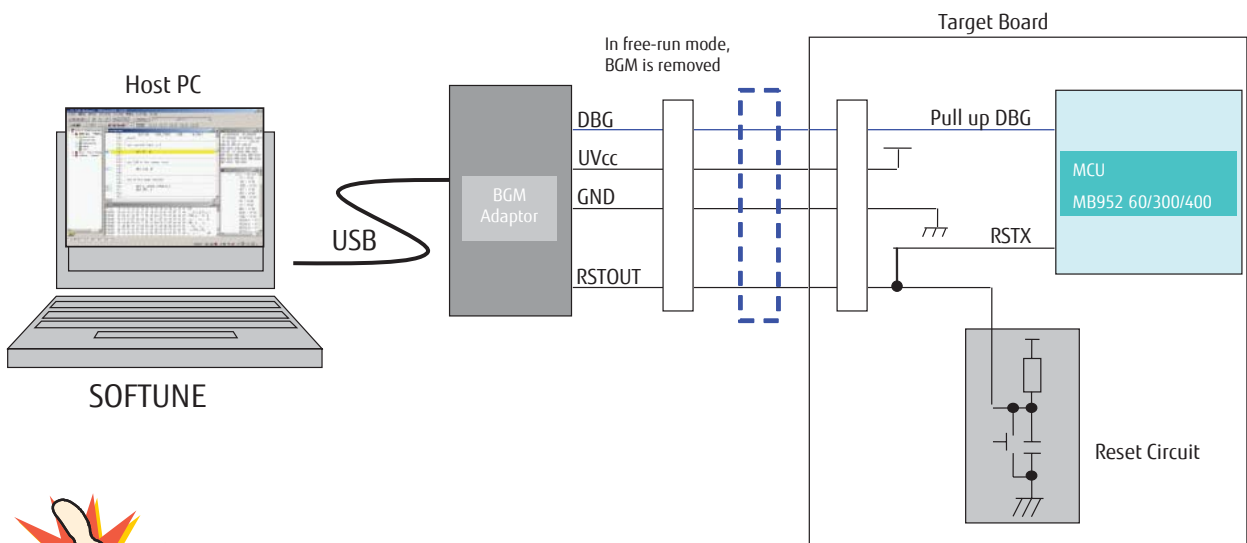
Easy to use



Easy-to-use development environment

Support MCU Series	STK PN	BGMA P/N	EV-board PN
MB95260 series	MB2146-420A-01-E	MB2146-07-E	NA
MB95330 series	NA		MB2146-440-E
MB95390 series	NA		MB2146-440-E + MB2146-441-E(MCU adaptor)
MB95310 Series	NA		MB2146-450-E
MB95370 Series	NA		MB2146-450-E + MB2146-451-E(MCU adaptor)
MB95350 Series	NA		MB2146-460-E
MB95430 series	NA		MB2146-480-E
MB95410/470 Series	NA		MB2146-470-E
MB95560 series	MB2146-510-01-E		NA
MB95630 series	NA		MB2146-440-E+ MB95F636 adaptor
MB95690 series	NA		MB2146-440-E+MB95F636 adaptor
MB95818 series	NA		T.B.D

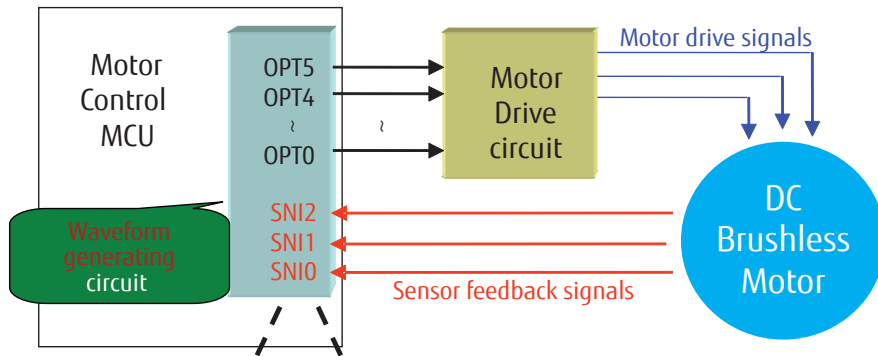
Debug Interface



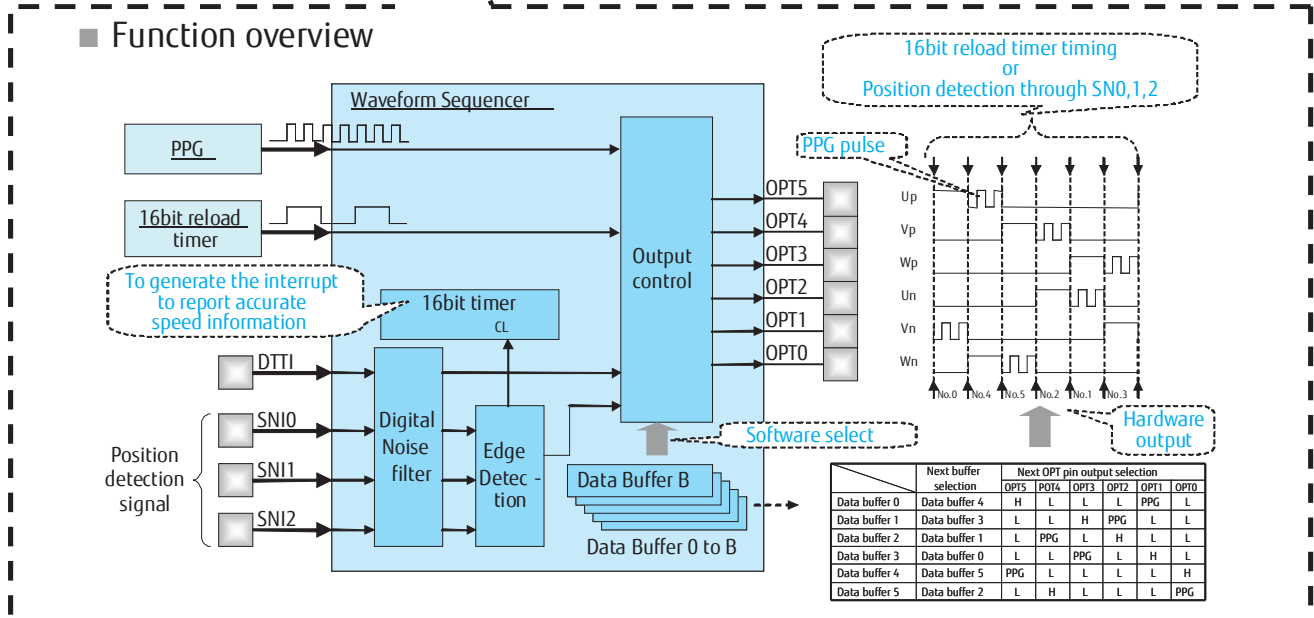
- ★ No occupy any user resources on MCU (using built-in OCD)
- ★ Only connected to the DBG pin of MCU (via a single serial wire control)
- ★ Support directly debug with mass product chip, without using EVA chip
- ★ Supports Flash-on-board programming

### Waveform Generator Circuit (MB95330H/390H Series)

Example of connecting with brushless DC motor



#### Function overview



### LCD Controller Circuit (MB95310/370 Series)

